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UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Troy STOCKSTAD	Attorney Docket:	60178-300402
Serial No.:	Not yet assigned	Group Art Unit:	Not yet assigned
Filed:	December 4, 2001	Examiner:	Not yet assigned
Title:	LOW VOLTAGE RAIL-TO-RAIL CMOS OUTPUT STAGE		



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INFORMATION DISCLOSURE STATEMENT

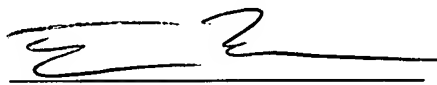
Applicant submits herewith the references listed on the attached form PTO-1449 of which Applicant is aware which are believed to be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 CFR 1.97(g)), nor as an admission that the information cited is, or is considered to be, material to patentability, nor an admission that no other material information exists.

The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

Respectfully submitted,

DATE: December 4, 2001



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